24th IEEE International SoC Conference (SOCC)
September 26 – 28, 2011
Grand Hotel – Taipei, Taiwan, ROC

System on Chip (SoC) has been the enabling technology behind the evolution of many of today's ubiquitous technologies, such as Internet, mobile wireless technology, and high definition television. The information technology age, in turn, has fuelled a global communications revolution. SoC devices today include some or all of the major electronics technologies, comprising RF, analog, optical, digital, and Nano-Electro-Mechanical Systems (NEMS). Recent advances in systems, deep-submicron and nano process technology are unleashing new opportunities, but also creating new challenges. Managements of design and verification complexity, EDA tools, on-chip communication, and design reuse for SoCs are significant complications, and SoC fault detection is becoming more difficult as chip size is increasing.

The SOC Conference is a premier forum for sharing advances in SoC technologies and applications in the areas of digital systems, circuit architectures, design methods, tools, automation, manufacturing, test, and emerging technologies. The 24th SOCC will be held in Taipei, Taiwan, and will offer three days of technical papers, technical workshops, and a vendor exhibition. For updates, please check our website regularly http://www.ieee-socc.org or follow us on twitter "@ieee_socc" (or http://twitter.com/ieee_socc).

SUBMISSION OF PAPER AND WORKSHOP/TUTORIAL PROPOSALS

Electronic paper submissions require a full paper in *pdf* format, limited to **six** double-column IEEE format pages, and including figures and references. **Paper submission deadline is April 11, 2011**. SOCC 2011 also welcomes embedded tutorial proposals in the related areas of SoC. A proposal with title, a half-page summary, and speaker's short bio should be submitted to the Tutorial Chair, Dr. Nagi Naganathan (Nagi.Naganathan@lsi.com), by **May 3, 2011.** The SOCC proceedings will be published on **IEEE *Xplore®*.**

SPONSORSHIP OPPORTUNITIES

IEEE SoC Conference invites sponsorship from corporations, government and academia at many levels. For sponsorship and exhibition, please contact the SoC Conference office at info@ieee-socc.org

SOCC TECHNICAL SCOPE

Papers are invited which address new and previously unpublished results in the area of Systems on Chip, and related topics.
• Analog, Mixed-Signal, and Biomedical Circuits and Systems
• Wireline and Wireless Communication Circuits and Systems
• Digital Signal Processing (DSP) Circuits and Systems (*Video, Multimedia, Software-Defined Radio, etc.*)
• Green Circuits, Systems, and Design Methodologies
• Embedded Systems, Multi/Many Core Systems and Embedded Memory Technologies
• Network on Chip (NoC), Interconnects, and 3D-IC
• Reconfigurable and Programmable Circuits and Systems, System on Programmable Devices (*FPGAs*)
• System Level Design Methodology, EDA and Design Tools for SoC
• Signal Integrity, Design for Testability, and Design Verification
• Design for Manufacturability, Variation-Aware Methodologies

IMPORTANT DATES

Paper submission deadline: April 11, 2011
Notification of acceptance: June 13, 2011
Final camera-ready paper due: July 11, 2011

ORGANIZING COMMITTEE

Conference General Chairs:	*Sao-Jie Chen, National Taiwan Univ.*	Honorary Chair:	*Liang-Gee, Chen, National Taiwan Univ.*
	Andrew Marshall, Texas Instruments	Steering Committee Chair:	*Thomas Buechner, IBM*
Technical Program Chair:	*Ramalingam Sridhar, SUNY Buffalo*	Europe Liaison:	*Sakir Sezer, Queen's Univ. , Bellfast*
Technical Program Co-Chair:	*Norbert Schuhmann, Fraunhofer IIS*	Publicity Chair:	*Kaijian Shi, Synopsys*

Please visit the conference web site http://www.ieee-socc.org or contact the SoC Conference office at info@ieee-socc.org for more information.