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IEEE CIRCUITS AND SYSTEMS SOCIETY



24th IEEE International SoC Conference (SOCC)

September 26 – 28, 2011

Grand Hotel – Taipei, Taiwan, ROC

System on Chip (SoC) has been the enabling technology behind the evolution of many of today's ubiquitous technologies, such as Internet, mobile wireless technology, and high definition television. The information technology age, in turn, has fuelled a global communications revolution. SoC devices today include some or all of the major electronics technologies, comprising RF, analog, optical, digital, and Nano-Electro-Mechanical Systems (NEMS). Recent advances in systems, deep-submicron and nano process technology are unleashing new opportunities, but also creating new challenges. Managements of design and verification complexity, EDA tools, on-chip communication, and design reuse for SoCs are significant complications, and SoC fault detection is becoming more difficult as chip size is increasing.

The SOC Conference is a premier forum for sharing advances in SoC technologies and applications in the areas of digital systems, circuit architectures, design methods, tools, automation, manufacturing, test, and emerging technologies. The 24th SOCC will be held in Taipei, Taiwan, and will offer three days of technical papers, technical workshops, and a vendor exhibition. For updates, please check our website regularly <http://www.ieee-socc.org> or follow us on twitter "@ieee_socc" (or http://twitter.com/ieee_socc).

HIGHLIGHTS

Keynote	Yale N. Patt Professor of Electrical and Computer Engineering Ernest Cockrell, Jr. Centennial Chair in Engineering The University of Texas at Austin	The Future Microprocessor: Return of the ASIC
Plenary I	Giovanni De Micheli Professor and Director Institute of Electrical Engineering, Integrated Systems Centre EPF Lausanne, Switzerland	Nanosystems: technologies and architectures for sensing and computing
Plenary II	Chih-Kung Lee President, Institute for Information Industry, Taiwan Lifetime Distinguished Professor Institute of Applied Mechanics and Dept. of Engineering Science and Ocean Engineering National Taiwan University	From Global Telecare Development Trends to Taiwan's Biomedical Electronics: Opportunity and Outlook
Plenary III	Gideon Intrater VP of Product Marketing and Applications MIPS Technologies	Boosting Performance Efficiency in Multiprocessor Systems through Multi-Threading
Luncheon Speech	Laung-Terng Wang President & CEO, Syntest Technologies, Inc.	Introduction to SoC Testing

IMPORTANT DATES

Early registration: By August 31, 2011

ORGANIZING COMMITTEE

Conference General Chairs: *Sao-Jie Chen, National Taiwan Univ.*
Andrew Marshall, Texas Instruments
Technical Program Chair: *Ramalingam Sridhar, SUNY Buffalo*
Technical Program Co-Chair: *Norbert Schuhmann, Fraunhofer IIS*

Honorary Chair: *Liang-Gee, Chen, National Taiwan Univ.*
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Please visit the conference web site <http://www.ieee-socc.org> or contact the SoC Conference office at info@ieee-socc.org for more information.