www.ieee-socc.org SOCC 2014





Committee

G eneral C hair

Kaijian Shi Cadence Design Systems

Technical Program Chair

Thomas Buechner IBM

TPC Co-Chair

Danella Zhao Univ. of Louisiana at Lafayette

Financial/Publication Chair

Ramalingam Sridhar SUNY at Buffalo

Tutorial Chair

Yvejian W v Infinera

Publicity Chair

Andrew Marshall
University of Texas at Dallas

Sponsorship/Exhibition Chair

Karan Bhatia

Texas Instruments

Local O C Chair

Venki Muthukumar Univ. of Nevada Las Vegas

Steering Committee Chair

N orbert Schuhmann Fraunhofer IIS

Europe Liaison

Sakir Sezer

Q ueen's University, Belfast

A sia-Pacific Liaison

Sao-Jie Chen National Taiwan University



SOCC 2014 - Call for Participation

The IEEE SoC Conference (SOCC) is a premier forum for networking with leaders in the field of SoC and establishing collaborative research. It is an ideal opportunity for sharing advances in SoC technologies and applications in the areas of digital systems, circuit architectures, computing platforms, design methods, automation, test, and emerging technologies.

SOCC2014 offers a four-day technical program featuring:

Keynote and plenary speakers from industry leaders

- ◆ Tom Beckley (Sr. VP of R&D for Custom IC and Simulation, Cadence) "The Internet of Every-Thing: EDA Perspectives"
- ◆ Scott Runner (VP of Advanced Methodologies and Low-Power Design,

Qualcomm) "SoCs for Mobile Applications: Systems from 0 MPH to over 100 MPH"

◆ J. Thomas Pawlowski (Fellow and Chief Technologist, Micron Technology)

"The Future of Memory/Logic Technologies and Computing System Architectures"

Tutorial Day with 8 sessions covering various hot SoC topics

A high-profile panel session on the future of SoC

Technical Presentations of new and unpublished research results in the areas:

- Analog and Mixed Signal Circuits and Systems
- Biomedical Circuits and Systems
- Wireline and Wireless Communication Circuits and Systems
- Digital Signal Processing (DSP) Circuits and Systems
- Low-Power Circuits, Systems, and Design Methodologies
- Embedded Systems, Multi Core Systems & Embedded Memory
- Network on Chip (NoC), Interconnects, and 3D IC
- Reconfigurable and Programmable Circuits and Systems
- System Level Design Methodology and Tools
- Design Verification, DFT and DFM
- Advanced industry techniques in SoC

High calibre social events program, including Las Vegas show, banquet, and tour of the cavernous power house of Hoover Dam hydro-electric plant.

SOCC2014 encourages the attendance of students for the opportunities to advance their knowledge in the SoC area. SOCC2014 offers student travel and student tutorial grants sponsored by IEEE CASS Outreach Initiative, to eligible SOCC2014 attendees. See details on the main website: http://www.ieee-socc.org

Contact us at info@ieee-socc.org, follow us on twitter@ieee_socc, and join us in LinkedIn SOCC Group.