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The International System-on-Chip Conference (SOCC), sponsored by the IEEE Circuits and Systems Society, is the premier forum for presenting the latest technical advancements and research findings in SoC process technology, circuit design, architecture, systems, test, design tools, and applications. SOCC 2015 offers a four-day technical program including keynote and plenary speeches, oral and poster presentations, hot-topic panel sessions, tutorials, and exhibitions. The SOCC is also soliciting submissions for the “Design Track” as well as “Special Session” proposals. The intent of the “Design Track” is to provide a forum for engineers in industry to share their expert knowledge and showcase their cutting-edge work in solving issues plaguing a hot area of SoC development. The “Special Session” proposals are invited but will face the same peer review as regular papers to ensure high quality. For more information, contact us: info@ieee-socc.org.

Area of Interests

Papers are invited which address new and previously unpublished results in all areas related to SoC which include, but are not limited to:

- **Circuits and Systems:** RF, analog and mixed-signal; Biomedical; Wireline and wireless communication; Digital signal processing (DSP); Memory systems; Reconfigurable and programmable;
- **Low Power Design:** “Green” circuits and systems; Low power design methodologies; Power/energy/thermal aware architecture design; Multi-domain power/energy management
- **MPSoC Architecture:** On-chip interconnect; Network on Chip (NoC) and multicore architectures; Memory architecture for multicore computing; 3D-IC; Heterogeneous computing; Parallel programming and software models
- **Design Methodologies:** HW-SW codesign, reconfiguration and debug; System level design methodology and tools; Design validation and verification; Design for Testability, test synthesis, embedded test for MPSoC
- **Application Specific Design:** SoC for driver assistant systems; Embedded computing and Internet of Things; High-performance mobile SoCs; Security; Real-time, high reliability and safety SoCs; Imaging, vision, and cloud
- **Emerging and Evolutionary Design:** Many-core architectures; General purpose GPU (GPGPU) computing; FPGA accelerated computing; Server on a Chip; Cortical processor and neuromorphic computing; Futuristic development and optimization tools

Submission of Papers and Workshop/Tutorial/Special Session Proposals

All papers and proposals must be in pdf format. For detailed formatting instructions and submission guidelines, please see below and refer to www.ieee-socc.org.

- * **Regular Papers:** Must be submitted through the SOCC2015 website. All papers are limited to six (6) double-column IEEE formatted pages. Paper submissions will go through a double-blind peer review process. Accepted papers will be included in the SOCC proceedings and will be submitted for publication in IEEE Xplore®.
- * **Workshop/Tutorial Proposals:** Must include title, one-page abstract/summary, and speaker's bio. All proposals must be submitted directly to the Tutorial Chair.
- * **Special Session Proposals:** Must include title, topic rationale, organizer's short bio, and a list of contributed papers. Proposals must be submitted directly to the Special Session Chair.
- * **Design Track submissions:** Require a two-page extended abstract which will be published on the SOCC website and in the program. Please submit through the SOCC2015 website.

Key Dates

Special session proposal submission deadline: **March 10th, 2015**

Regular paper submission deadline: **March 31st, 2015**

Workshop/tutorial proposal submission deadline: **April 8th, 2015**

Design Track extended abstract due: April 14th, 2015

Notification of acceptance: June 10th, 2015

Final camera-ready paper due: June 30th, 2015